

<b>Notice of References Cited</b>	Application/Control No. 10/637,168		Applicant(s)/Patent Under Reexamination TREMBLAY ET AL.	
	Examiner Kaushikkumar Patel		Art Unit 2188	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-2002/0178349 A1	11-2002	Shibayama et al.	712/235
*	B	US-2003/0079094 A1	04-2003	Rajwar et al.	711/150
*	C	US-6,681,311 B2	01-2004	Gaskins et al.	711/203
*	D	US-6,941,449 B2	09-2005	Ross, Jonathan K.	712/35
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"Speculative Lock Elision: Enabling Highly Concurrent Multithreaded Execution" by Rajwar and Goodman. Preceedings of the 34th Annual ACM/IEEE International Symposium on Microarchitecture, December, 2001. Pages 294-305
	V	"the Transaction Concept: Virtues and Limitations" by Jim Gray. Pro. Int'l. Conf. Very Large Databases, Morgan Kauffman, 1981. pages 144-154
	W	"Toward Efficient and Robust Software Speculative parallelization on Multiprocessors" by Marcelo Cintra and Diego R. Lianos, PPOPP' 03 June 11-13, 2003 ACM 1-58113-588-2/03/0006 pages 13-24.
	X	Microsoft Computer Dictionary Fifth Edition, pub. 2002 page 378.

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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